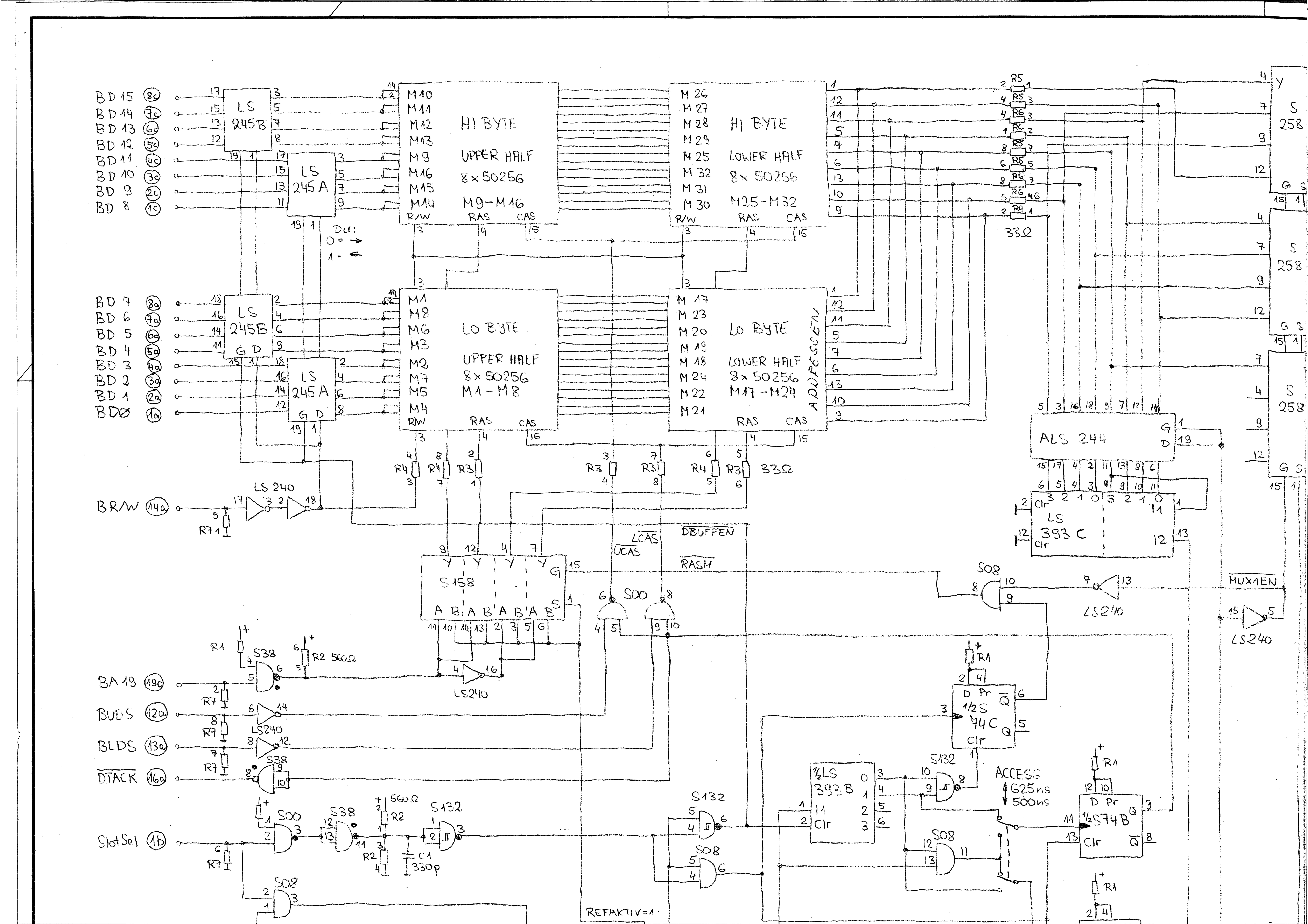
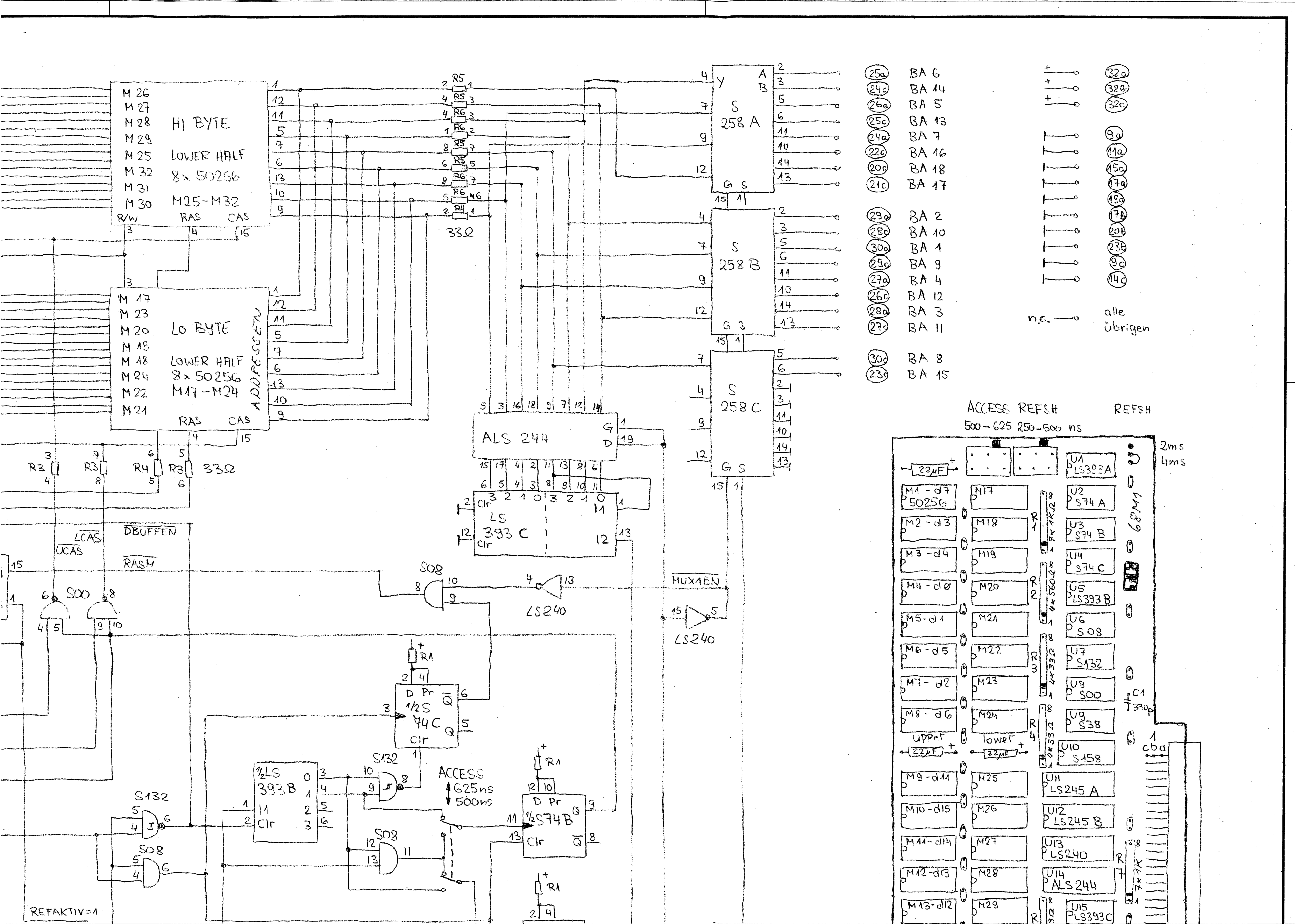
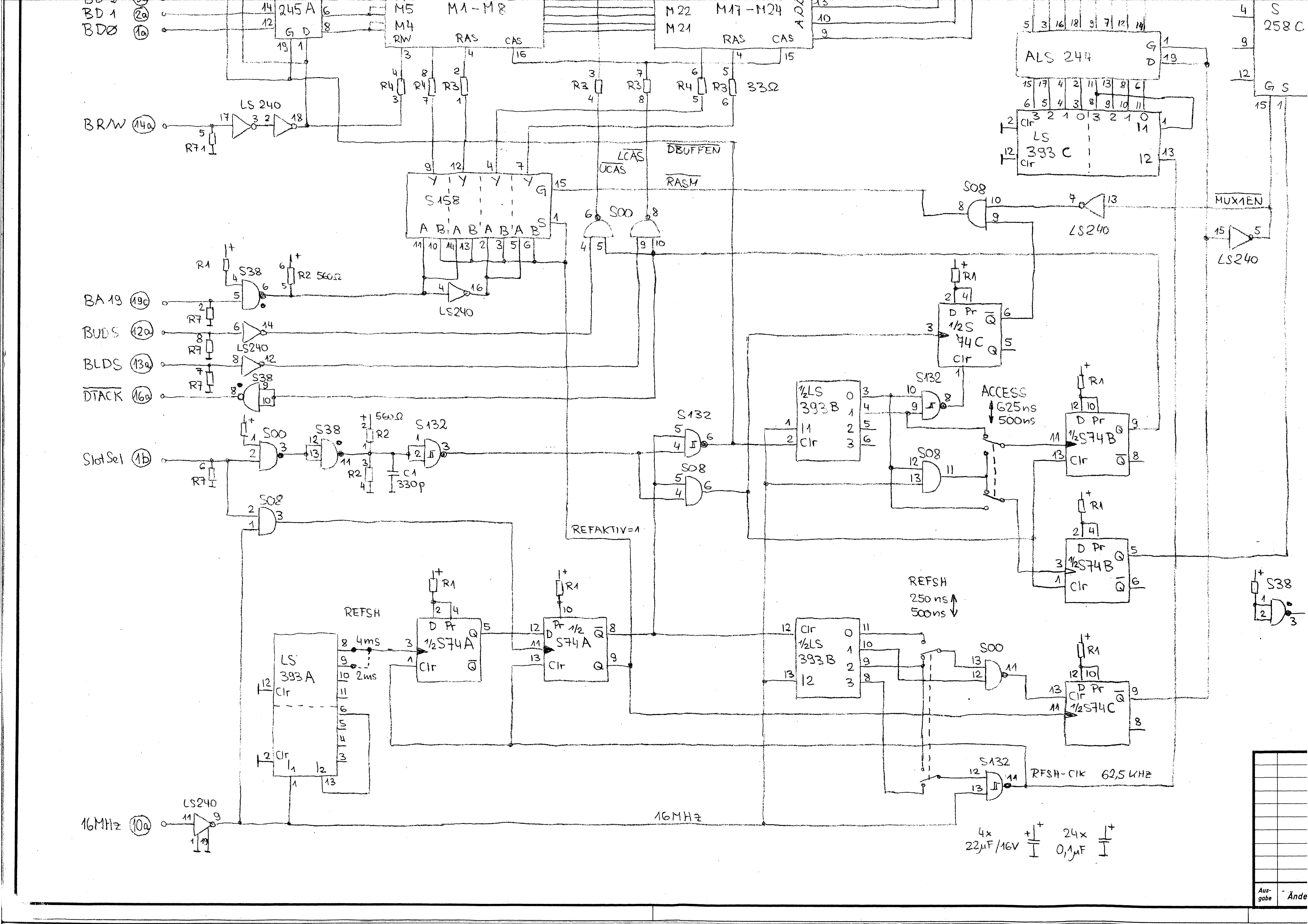
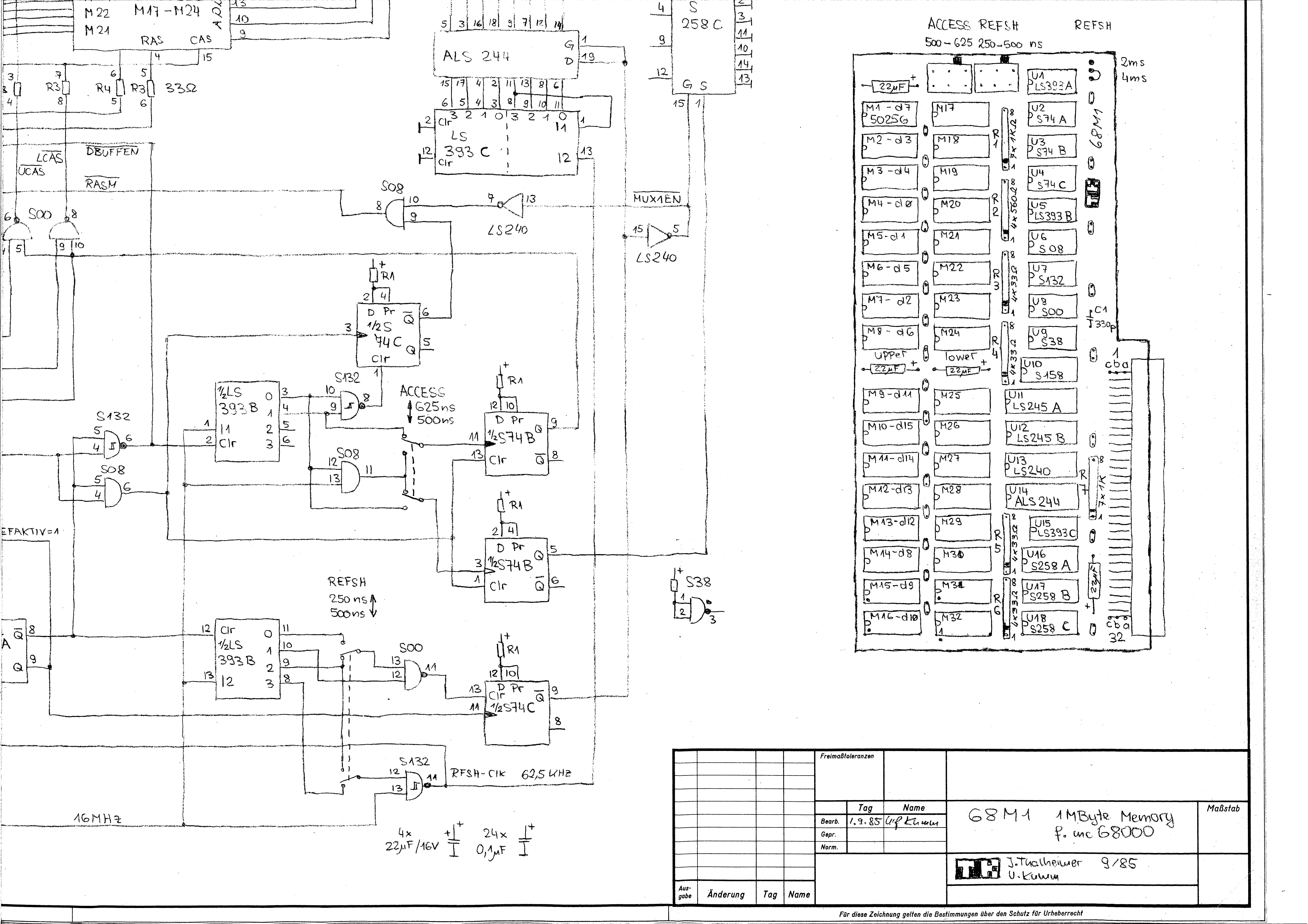

BD 15 (8c)
BD 14 (7c)
BD 13 (6c)
BD 12 (5c)
BD 11 (4c)
BD 10 (3c)
BD 9 (2c)
BD 8 (1c)
LS 245B
LS 245A
HI BYTE
UPPER HALF
8 × 50256
M9–M16
HI BYTE
LOWER HALF
8 × 50256
M25–M32
R/W
RAS
CAS
Dir:
0 = →
1 = ←
BD 7 (8a)
BD 6 (7a)
BD 5 (6a)
BD 4 (5a)
BD 3 (4a)
BD 2 (3a)
BD 1 (2a)
BD0 (1a)
LO BYTE
UPPER HALF
8 × 50256
M1–M8
LO BYTE
LOWER HALF
8 × 50256
M17–M24
ADDRESSEN
33Ω
ALS 244
LS 393 C
S 258
BR/W (14a)
LS 240
S158
S00
S08
LCAS
UCAS
DBUFFEN
RASM
MUX1EN
BA 19 (19c)
BUDS (12a)
BLDS (13a)
DTACK (16a)
SlotSel (1b)
S38
R2 560Ω
LS240
1/2 S 74C
S132
1/2 LS 393B
ACCESS
625ns
500ns
1/2 S74B
C1 330p
REFAKTIV=1



HI BYTE
LOWER HALF
8x 50256
M25-M32
LO BYTE
LOWER HALF
8x 50256
M17-M24
ADDRESSEN
RAS
CAS
R/W
33Ω
S 258 A
S 258 B
S 258 C
ALS 244
LS 393 C
BA 6
BA 14
BA 5
BA 13
BA 7
BA 16
BA 18
BA 17
BA 2
BA 10
BA 1
BA 9
BA 4
BA 12
BA 3
BA 11
BA 8
BA 15
n.c.
alle übrigen
MUX1EN
LS240
S08
S00
S132
LCAS
UCAS
DBUFFEN
RASM
1/2 S 74 C
1/2 LS 393 B
1/2 S74 B
ACCESS
625ns
500ns
REFAKTIV=1
ACCESS REFSH
500-625 250-500 ns
REFSH
2ms
4ms
68M1
22µF
upper
lower
C1
330p
U1 LS393 A
U2 S74 A
U3 S74 B
U4 S74 C
U5 LS393 B
U6 S08
U7 S132
U8 S00
U9 S38
U10 S158
U11 LS245 A
U12 LS245 B
U13 LS240
U14 ALS 244
U15 LS393C



BD 1
BDØ
245 A
M5
M4
M1 – M8
RW
RAS
CAS
M22
M21
M17 – M24
ALS 244
258 C
LS 393 C
LS 240
BR/W
R71
S158
LCAS
UCAS
DBUFFEN
RASM
S08
S00
MUX1EN
LS240
S38
BA 19
BUDS
BLDS
DTACK
R2 560Ω
1/2 S 74 C
1/2 LS 393 B
S132
ACCESS
625 ns
500 ns
1/2 S74 B
SlotSel
C1 330p
REFAKTIV=1
REFSH
LS 393 A
4ms
2ms
1/2 S74 A
250 ns
500 ns
1/2 S74 C
RFSH-Clk 62,5 kHz
16MHz
4x 22µF/16V
24x 0,1µF
Aus-gabe
Ände


M22
M21
M17 – M24
RAS
CAS
R3
R4
33Ω
LCAS
UCAS
DBUFFEN
RASH
S00
ALS 244
LS 393 C
S 258 C
S08
LS240
MUX1EN
R1
1/2 S 74 C
S132
1/2 LS 393 B
ACCESS
625ns
500ns
1/2 S74 B
REFAKTIV=1
REFSH
250 ns
500 ns
S38
RFSH-Clk 62,5 kHz
16MHz
4x 22µF/16V
24x 0,1µF
ACCESS REFSH REFSH
500 – 625 250–500 ns
2ms
4ms
22µF
U1 LS393A
U2 S74 A
U3 S74 B
U4 S74 C
U5 LS393 B
U6 S08
U7 S132
U8 S00
U9 S38
U10 S158
U11 LS245 A
U12 LS245 B
U13 LS240
U14 ALS 244
U15 LS393C
U16 S258 A
U17 S258 B
U18 S258 C
M1 - d7 50256
M2 - d3
M3 - d4
M4 - d0
M5 - d1
M6 - d5
M7 - d2
M8 - d6
M9 - d11
M10 - d15
M11 - d14
M12 - d13
M13 - d12
M14 - d8
M15 - d9
M16 - d10
M17
M18
M19
M20
M21
M22
M23
M24
M25
M26
M27
M28
M29
M30
M31
M32
upper
lower
68M1
C1 330p
7x 1KΩ
4x 560Ω
4x 33Ω
c b a
Freimaßtoleranzen
Tag
Name
Bearb. 1.9.85
Gepr.
Norm.
68M1 1MByte Memory f. mc 68000
Maßstab
J. Thalheimer 9/85
U. Kumm
Ausgabe
Änderung
Tag
Name
Für diese Zeichnung gelten die Bestimmungen über den Schutz für Urheberrecht